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Today, Jun 26

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Jun 25, 2006
posynomial function
- ☆ CMOS Operational Amplifier Design and Optimization via Geometric ... - 5:04pm
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- ☆ CMOS Operational Amplifier Design and Optimization via Geometric ... - 5:03pm
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posynomial, what is - Related history
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- ☆ Design of Posynomial Models for Mosfets: Symbolic Regression Using ... - 5:01pm
  people.csail.mit.edu/unamay/publications-dir/rptp06.pdf
- ☆ Solving Posynomial Geometric Programming Problems via Generalized ... - 5:01pm
  portal.acm.org/citation.cfm?id=585065
- ☆ http://doi.ieeecomputersociety.org/10.1109/DATE.2003.1186394 - 5:01pm

posynomial function
- ☆ Wireless Design & Development - 4:52pm
  www.wirelessdesignmag.com/PRArchivebyIssue.aspx?RELTYPE...

Jun 23, 2006
Product configuration using Object oriented grammars
- ☆ Product Configuration Using Object Oriented Grammars - 4:10pm
  www.cs.lth.se/Research/ProgEnvrPapers/SCM98.GH.pdf

an introduction to Binary decision Diagram
- ☆ An Introduction to Binary Decision Diagrams - 2:04pm
  www.cs.unb.ca/~gdueck/courses/cs4835/bdd97.pdf

Searches with no clicked results:
Product configuration using Object oriented grammars, Loose Interface Definition: An Extended Interface Definition for ..., Loose Interface Definition: An Extended Interface, symposium system configuration management scm7, symposium system configuration management scm7

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EAST Search History

L21	1	"6314390 ".pn.	USPAT	OR	OFF	2006/06/26 10:16
L22	4936	(performance adj parameter) and (simulat$4 model$4)	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L23	1863	L22 and (performance with (rang$4 bound limit$4))	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L24	16	BSIM and (DEPICT SUPREM)	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L25	415	L22 and (device with (simulat$4 model$$)) and (process with (simulat$4 model$4))	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L26	1579	(performance adj parameter) with (range bound limit$ condition)	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L27	2	L26 and (device adj model$4) and (process$4 adj model$4)	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L28	5	L26 and (device adj model$4 simulat$4)) and (process$4 adj model$4 simulat$))	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L29	769	L26 and (simulat$4 model$)	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L30	405	L29 and (semiconductor integrated)	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L31	240	L29 and (semiconductor (integrated adj circuit))	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L32	269	L29 and (semiconductor IC (integrated adj circuit))	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L33	8	("6028994").URPN.	USPAT	OR	OFF	2006/06/26 10:16
L34	2	"10/023235"	US-PGPUB	OR	OFF	2006/06/26 10:16
L35	894	703/13.ccls.	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16
L36	15	(US-20020059339-$).did. or (US-5282128-$ or US-5319564-$ or US-5646870-$ or US-5655110-$ or US-5819208-$ or US-5966527-$ or US-6028994-$ or US-6249712-$ or US-6269277-$ or US-6298470-$ or US-6311096-$ or US-6407611-$ or US-6598210-$ or US-6795952-$).did.	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	373	(design with variation) and (process with variation) and (simulat$4 model$4)	USPAT	OR	OFF	2006/06/26 10:16
L2	12	(design adj variation) and (process adj variation) and (simulat$4 model$4)	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16
L3	173	(design adj var$8) and (process adj var$8) and (simulat$4 model$4)	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16
L4	20434	(performance with parameter) and (simulat$4 model$4)	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L5	4936	(performance adj parameter) and (simulat$4 model$4)	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L6	1863	L5 and (performance with (rang$4 bound limit$4))	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L7	50	("5646870").URPN.	USPAT	OR	OFF	2006/06/26 10:16
L8	8	process with design with correlat$4 with parameter	USPAT	OR	OFF	2006/06/26 10:16
L9	14	process with design with correlat$4 with parameter	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16
L10	36	process with design with correlat$4 with parameter	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L11	22	L10 not L9	US-PGPUB; USPAT	OR	ON	2006/06/26 10:16
L12	1	"10/90307"	US-PGPUB	OR	ON	2006/06/26 10:16
L13	0	"WILLIAMS; RICHARD Q.".in.	USPAT	OR	OFF	2006/06/26 10:16
L14	0	"WILLIAMS; RICHARD Q.".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/26 10:16
L15	0	"WILLIAMS RICHARD Q.".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/26 10:16
L16	0	"WILLIAMS, RICHARD Q.".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/26 10:16
L17	0	"RICHARD Q. WILLIAMS".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/26 10:16
L18	19	"RICHARD WILLIAMS".in.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/26 10:16
L19	3	"6329690"	USPAT	OR	OFF	2006/06/26 10:16
L20	1	"6329690".pn.	USPAT	OR	OFF	2006/06/26 10:16

EAST Search History

L37	48	("5860032" "5884050" "5921896" "5931136" "5968153" "5986845" "591043" "6007203" "6029000" "6070992" "6084386" "6111832" "6115772" "6142410" "6161161" "6173015" "6208284" "6218274" "6219787" "6218274" "6219787" "6243610" "6247069" "6266731" "6344732" "6346807" "6396137" "6419636" "6467282" "6626144" "6721859" "6727977" "6775605" "6819991" "6825631" "6944704" "5978860" "5007736" "5279276" "5540821" "5984475" "6088095" "6088038" "6188786" "4244583" "4318330" "4320703" "4345252" "4347679" "4367934").pn.	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16
L38	50	("5487167" "4630845" "5540216" "5576973" "5727125" "4265720" "4295528" "4342032" "445968" "4470351" "4477079" "4533918" "4782223" "4784523" "4817954" "4852153" "4857987" "4891597" "4937437" "4953443" "4982621" "4985887" "5179700" "5191411" "5204951" "5214653" "5223661" "5249500" "5257368" "5261970" "5267179" "5405108" "5423048" "5442376" "5485139" "5499378" "5521464" "5521579" "5566317" "5575072" "5581594" "5602959" "5699775" "5721880" "5748981" "5765189" "5794186" "5798518" "5811721" "5832130").pn.	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16
L39	728	(output adj resistance) with (transistor)	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16
L40	7	L39 and (lumped adj capacitance)	US-PGPUB; USPAT	OR	OFF	2006/06/26 10:16